

New Methodology for the Prediction of Motor Starting Effect on Bus Voltages of Interconnected Power Systems

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Abstract A new methodology is proposed in this paper capable of predicting the impact of induction motor load starting on the bus voltages of an interconnected power system. The profile of the voltage sag is predicted for each bus, which is used to improve the power quality of a system. The methodology is investigated with the use of a four bus, as well as fourteen bus IEEE interconnected system. Mathematical and simulation results demonstrate the effectiveness and applicability of the proposed methodology for the application.

Keywords: voltage sag prediction, induction motor starting, power quality

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1. Introduction

Voltage sag is defined as an rms variation with a magnitude between 10 % and 90 % of nominal and a duration between 0.5 cycles and one minute [1], and is considered to be one of the most serious power quality issues. Voltage sags are caused by a increase in transient current, usually appearing during motor starting, transformer energizing, load switching and faults. The probability of equipment failure due to voltage sag and power system quality variations in general increases dramatically with increased sensitivity. As technology evolves, equipment such as adjustable speed drives, process control equipment and computers are designed with the use of highly sensitive modern power electronics and inevitably become more vulnerable.

The influence of induction motor loads on the characterization of voltage sags is of great importance, as indicated by researchers [2-10]. Motors decelerate during a short circuit, and for a time interval behave as generators, maintaining a voltage supplied by its internal or back electromotive force (e.m.f). After fault clearance, the motor will accelerate again, drawing a high reactive current from the supply, until the steady-state speed is reached, causing prolonged post fault voltage sag. The deceleration and acceleration of motors influences the duration and shape of voltage sags. Most studies characterizing and predicting voltage sags are based on statistical data describing the frequency of occurrence, the magnitude and duration of the sags [11,12,13,14]. Recent studies have produced voltage sag indices mathematically describing the sag and providing tools for online calculation techniques [15,16,17]. Those indices however

are applicable on radial systems. Once the power system under investigation becomes interconnected, the application of those indices is no longer easily performed. Thus the need of a methodology, which can be used to predict voltage sags caused by large load switching and motor starting at interconnected systems, arises.

2. Methodology of Analysis

The purpose of the methodology presented is to predict the depth, duration, and the profile of a voltage sag sensed on the buses of an interconnected power system due to large induction motor load switching on one of the system buses. The large induction motor load used in the investigation is developed and verified in [6]. The proposed methodology consists of three steps [18]:

- Step 1 - Calculation of ZPCC (Impedance at point of common coupling).
- Step 2 - Obtaining power waveforms.
- Step 3 - Instantaneous load flow calculation.

The methodology proposed is shown on a flowchart depicted in Figure 1, and theoretical considerations regarding each step are discussed in the following sections.

2.1. Step 1- Calculation of ZPCC

The power system transmission lines data are used in order to calculate the impedance at the point of common coupling (ZPCC) in this step. Each bus of the system has its own characteristic ZPCC. $Y-\Delta$ and $\Delta-Y$ transformation can be used to reduce the impedances between the equivalent generator bus and the load bus, identifying the value of ZPCC. Figure 2 and equation (1) illustrates the principle of $Y-\Delta$ and $\Delta-Y$ transformation.

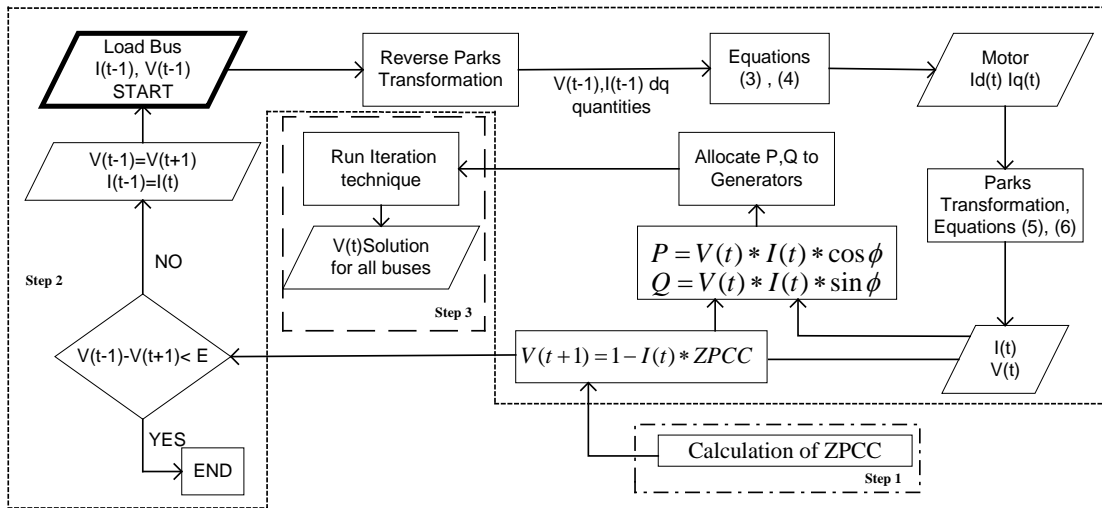


Figure 1. Flow chart representation of the methodology used in producing the transient voltage response on the system buses

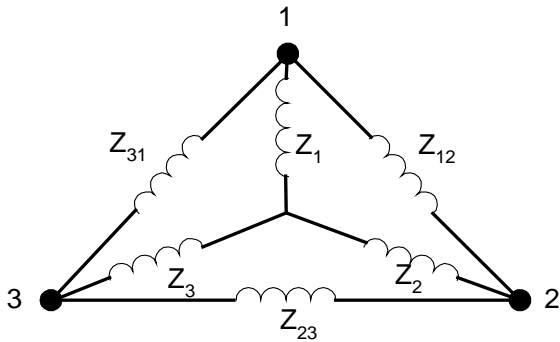


Figure 2. Y - Δ and Δ - Y transformation

The equations used in the transformations, referring to Figure 1 are shown in equations (1) - (2).

$$Z_{12} = Z_1 + Z_2 + \frac{Z_1 \times Z_2}{Z_3} \tag{1}$$

$$Z_{23} = Z_2 + Z_3 + \frac{Z_2 \times Z_3}{Z_1} \tag{1}$$

$$Z_{31} = Z_3 + Z_1 + \frac{Z_3 \times Z_1}{Z_2} \tag{1}$$

$$Z_1 = \frac{Z_{12} \times Z_{13}}{Z_{12} + Z_{13} + Z_{23}} \tag{2}$$

$$Z_2 = \frac{Z_{12} \times Z_{23}}{Z_{12} + Z_{13} + Z_{23}} \tag{2}$$

$$Z_3 = \frac{Z_{13} \times Z_{23}}{Z_{12} + Z_{13} + Z_{23}} \tag{2}$$

2.2. Step 2- Obtaining Power Waveforms

The ZPCC value calculated in the previous section can be used to represent the impedance between the generator and the bus which the load is being supplied by, as illustrated in Figure 3.

The test system shown in Figure 3 is used in order to identify the active and reactive power demand of the motor load. Equations (3) and (4) express the rate of change of a variable at time ‘t’ as a function of the value

of the same variable at time ‘t-1’. They describe the motor transient response in terms of direct and quadrature axis currents and voltages [12,13]. The parameters in the equations correspond to Figure 4. The motor load will be switched in an already energized system. The current flow and bus voltages before switching are known, as they are the steady state load flow solutions of the system. These voltages and currents are used in the first time step solution of equations (3)-(6) as the variables corresponding to time ‘t-1’. Thus the value of I_d and I_q parameters for time ‘t’ can be calculated (This is explained in *Appendix A: Basic Machine Theory*).

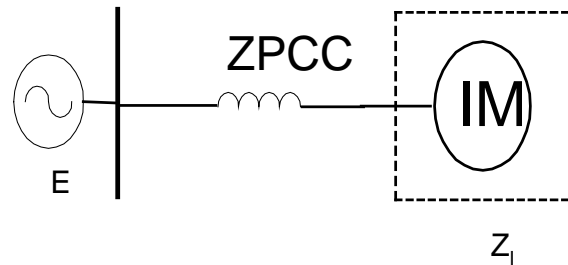


Figure 3. Equivalent circuit for power calculation

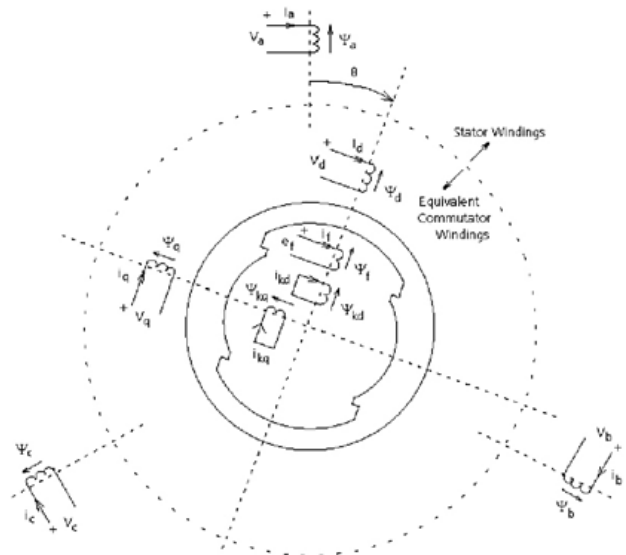


Figure 4. Induction motor d-q axis quantities

$$\frac{\partial}{\partial t} \begin{bmatrix} I_{d1} \\ I_{d2} \\ I_{d3} \end{bmatrix} = Ld^{-1} \times \begin{bmatrix} -u \cdot \Psi_q - R_{1d} \cdot I_{d1} \\ -R_{2d} \cdot I_{d2} \\ -R_{3d} \cdot I_{d3} \end{bmatrix} + Ld^{-1} \begin{bmatrix} V_{d1} \\ V_{d2} \\ V_{d3} \end{bmatrix} \quad (3)$$

$$\frac{\partial}{\partial t} \begin{bmatrix} I_{q1} \\ I_{q2} \\ I_{q3} \end{bmatrix} = Lq^{-1} \times \begin{bmatrix} -u \cdot \Psi_d - R_{1d} \cdot I_{q1} \\ -R_{2d} \cdot I_{q2} \\ -R_{3d} \cdot I_{q3} \end{bmatrix} + Lq^{-1} \begin{bmatrix} V_{q1} \\ V_{q2} \\ V_{q3} \end{bmatrix} \quad (4)$$

Using these values and Park’s reference frame transformation equations shown in equations (5) and (6), the values of the three phase voltages and currents can be obtained.

$$\begin{pmatrix} V_a \\ V_b \\ V_c \end{pmatrix} = \frac{2}{3} \begin{pmatrix} \cos \theta & \sin \theta & 1 \\ \cos \left(\theta - \frac{2\pi}{3} \right) & \sin \left(\theta - \frac{2\pi}{3} \right) & 1 \\ \cos \left(\theta - \frac{4\pi}{3} \right) & \sin \left(\theta - \frac{4\pi}{3} \right) & 1 \end{pmatrix} \times \begin{pmatrix} V_d \\ V_q \\ V_z \end{pmatrix} \quad (5)$$

$$\begin{pmatrix} I_a \\ I_b \\ I_c \end{pmatrix} = \frac{2}{3} \begin{pmatrix} \cos \theta & \sin \theta & 1 \\ \cos \left(\theta - \frac{2\pi}{3} \right) & \sin \left(\theta - \frac{2\pi}{3} \right) & 1 \\ \cos \left(\theta - \frac{4\pi}{3} \right) & \sin \left(\theta - \frac{4\pi}{3} \right) & 1 \end{pmatrix} \times \begin{pmatrix} I_d \\ I_q \\ I_z \end{pmatrix} \quad (6)$$

The values of active (P) and reactive power (Q) can be calculated for the particular time step using $P = VI \cos \theta$ and $Q = VI \sin \theta$, respectively. For the calculation of the next time step, ‘t+1’, the value of current at time ‘t’ has already been found in the previous step. As the per unit voltage at the motor terminals will be the one sent minus the voltage drop along ZPCC, the voltage value at time step ‘t’ can be calculated using.

The process defined above is repeated until the voltage and current variables vary in a rate less than the accepted error margin for the specific application and thus have reached steady state. Having obtained the active and reactive power demand of the motor, the values of active and reactive power, as well as transmission line impedance can be used to define the increment of power generated within the whole system. The increment of power can be allocated to the generators according to their MVA and their relative electrical distance from the load bus under consideration. The increment in power is then added to the steady state power generation pre-switching, which is known. Thus the active and reactive power transient waveforms of the induction motor and the system generators can be obtained, and are used in step three of the proposed methodology. The assumption made for the calculation of the active and reactive power waveforms is that the generators and the system do not enter an instability region during the transient period of switching, and the moment of inertia of all machines is the same. A more detailed model of the generators can be used to identify the generator response of a system with different parameters.

2.3. Step 3- Instantaneous Load Flow Calculation

Load flow is a methodology which provides a solution to a radial or meshed power system. There are various methods through which load flow solutions can be obtained. Typical iterative methods are Gauss-Seidel, Newton-Raphson, and Decoupled load flow method.

The load flow technique used in this step can be any of the above depending on the level of accuracy and speed required by the user for the application. It was decided for illustration reasons to use the Gauss-Seidel method as it has been widely used and it is easily applicable for a wide range of systems. In the Gauss-Seidel method, the system bus voltages are assumed at the first iteration, and the values of those quantities obtained during calculation are used in the second iteration. For this method at least one of the generator buses of the system is assumed to be an infinite bus with constant voltage and frequency. A generalize expression representing this method is shown in (7). The procedure of iterations is generally repeated until an acceptable error margin of variance appears for all unknown variables between two iterations.

$$V_i^{t+1} = \frac{1}{Y_{ii}} \left[\frac{S_i^*}{V_i^{*t}} - \left(\sum Y_{ij} \times V_j^t \right) \right] \quad (7)$$

where V is the voltage, t is the iteration step, i is the bus under consideration, j is the other buses in the system, Y is the admittance of transmission lines, and S is the bus apparent power.

The proposed methodology introduces the concept of inserting the transient response active and reactive power values obtained during Step 2, as discrete time step inputs in the load flow technique to produce the transient voltage response on the system buses. In large systems, the amount of data produced can be time consuming and also complicated. Thus a time step can be defined by the sensitivity of the customers connected to the system buses. A large increment of time step could cause a reduction in the accuracy of the resulting waveforms. Thus the depth and duration of the voltage sag inflicted by the motor switching on all buses can be obtained from the output of the series of load flow solutions.

In the next sections of this paper, an attempt is made to illustrate the validity and accuracy of the proposed method on interconnected systems using PSCAD [20] and Mathcad.

3. Methodology Application

The methodology proposed in the previous section is applied and verified with the use of two interconnected power system test cases. The first is a four bus interconnected power system, and the second is the IEEE 14 bus interconnected test system. The validation procedure and results obtained for the systems are shown in the following sections.

3.1. Four Bus Interconnected Power System

The four bus interconnected power system is shown in Figure 5. Buses 0 and 1 have generators connected to

them, whereas buses 2 and 3 are supplying loads. The voltage sag is caused in the system by the introduction of a new load on bus 2, (Z_1) consisting of induction motors. The four-bus system parameter datasheet is available upon request.

3.1.1. Step 1-Calculation of ZPCC

In order to calculate ZPCC, the power system impedances are reduced to an equivalent impedance between the load bus causing the sag (in this case bus 2) and the generator equivalent bus, as described in the previous section. The calculations performed to identify ZPCC were carried out in MathCAD. The ZPCC value of the load bus 2 is:

$$ZPCC = 0.021421325072895 + 0.061458071356768i \quad (8)$$

where the letter 'i' in MathCAD represents the imaginary notation 'j'.

3.1.2. Step 2- Obtaining Power Waveforms

In this section, the active and reactive power demand waveforms are obtained for the starting period of the induction motor individual generators' contribution also calculated. The value of ZPCC obtained in the previous section can be incorporated into a two-bus equivalent circuit as shown in Figure 3. The motor active and reactive power demand waveforms are obtained using equations (3)-(6) as described in a previous section. The calculated active and reactive motor load power demand with respect to time is shown in Figure 6. The active and reactive power waveforms shown in Figure 6 are causing the voltage sag, as they will be demanded on the load bus. Thus generators will have to increase their production to fulfill that demand.

The numerical value of the current, as well as active and reactive power load waveforms are used to produce the instantaneous values of the generator at specific time steps. The time step is decided according to the desired accuracy of the user. In the case where speed of calculation is preferred over accuracy the time step of the waveform used in the method can be increased. Table 1 illustrates the active and reactive power results calculated for G2, with G1 assumed to be infinite bus.

3.1.3. Step 3- Instantaneous Load Flow Calculation

Having established the active and reactive power numerical values for the motor and the system generators, the data are incorporated in MathCAD, where a mathematical program calculates the values of the resulting voltages for each iteration of Gauss-Seidel. When the error between the voltages of two consecutive iterations is less than the user pre-defined value, then the convergence criterions are fulfilled and a solution is reached. The load flow MathCAD program is shown in Figure 7. It can be seen from Figure 7 that the error of accuracy used while obtaining the results was 0.00001 %. In the case where speed of calculation is preferred over accuracy, the time step of the waveform used in the program can be increased and the error acceptability margin decreased.

For each time step of the input active and reactive power waveforms, a number of iterations are performed until the solution is reached. The next point on the

waveform is then inserted into the program as a numerical value and the process repeats itself until the complete waveform is plotted. The obtained results for the case studied in this section in terms of voltage level on the load buses during the voltage sag is presented in Table 2.

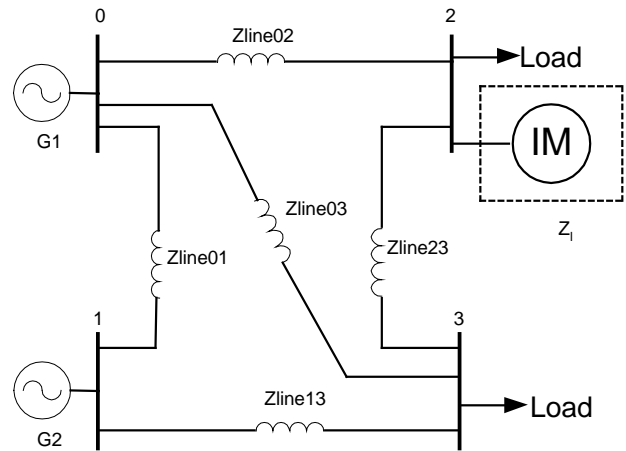


Figure 5. The Four bus interconnected system, with induction motor load on bus 2.

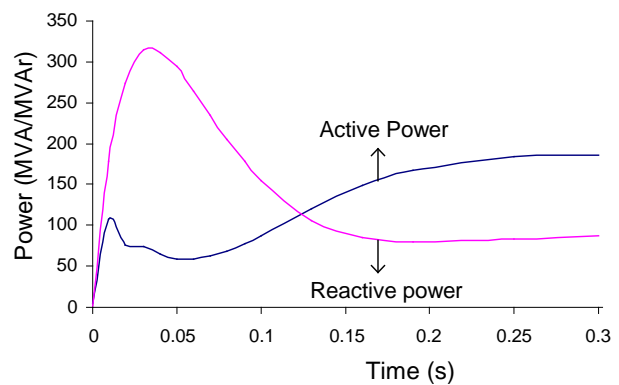


Figure 6. Calculated motor load power demand.

Table 1. Calculated G2 Power Values

Time(s)	Active Power (MW)	Reactive Power (MVAR)
0.00	3.150254	0.418937
0.01	19.327693	17.607060
0.02	12.179335	15.043000
0.03	13.913085	16.468555
0.04	10.562425	13.319292
0.05	10.252514	12.030956
0.06	11.555909	9.705550
0.07	12.946505	7.848863
0.08	17.192916	6.251093
0.09	20.384015	4.622207
0.10	25.076988	3.594140
0.13	38.778093	1.202682
0.16	49.076811	0.082967
0.19	55.333894	0.002283
0.25	61.263210	0.129332
0.30	61.979404	0.332849

```

prog(v, E) :=
  v1' ← 1/Y11pu · [ S1pu/v1 - (-Yline01pu · v0 + -Yline13pu · v3) ]
  v2' ← 1/Y22pu · [ S2pu/v2 - (-Yline02pu · v0 + -Yline23pu · v3) ]
  v3' ← 1/Y33pu · [ S3pu/v3 - (-Yline03pu · v0 + -Yline23pu · v2' + -Yline13pu · v1') ]
  while ( |v3' - v3| > E
        |v2' - v2| ≥ E
        |v1' - v1| > E
        )
  v1 ← v1'
  v2 ← v2'
  v3 ← v3'
  v1' ← 1/Y11pu · [ S1pu/v1 - (-Yline01pu · v0 + -Yline13pu · v3) ]
  v2' ← 1/Y22pu · [ S2pu/v2 - (-Yline02pu · v0 + -Yline23pu · v3) ]
  v3' ← 1/Y33pu · [ S3pu/v3 - (-Yline03pu · v0 + -Yline23pu · v2' + -Yline13pu · v1') ]
  v
  |prog(v, 0.000001)1| = 1.00212855293673
  |prog(v, 0.000001)2| = 0.959173975771377
  |prog(v, 0.000001)3| = 0.979709840788117
    
```

Figure 7. The load flow MathCAD program.

Table 2. Methodology Results

Time (s)	Voltage bus 2 (pu)	Voltage bus 3 (pu)
0.00	0.992700	0.997500
0.01	0.944070	0.972500
0.02	0.921700	0.961500
0.03	0.910098	0.955800
0.04	0.911839	0.956720
0.05	0.917560	0.959510
0.06	0.925690	0.963488
0.07	0.933750	0.967430
0.08	0.941168	0.971058
0.09	0.947456	0.974127
0.10	0.952507	0.976590
0.13	0.961360	0.980880
0.16	0.963800	0.982000
0.19	0.963200	0.981700
0.25	0.960520	0.980370
0.30	0.959170	0.979700

3.1.4. Step 4- Four Bus Results Verification

In order to verify the proposed methodology, the four-bus system was simulated in PSCAD. The values of the bus voltages were recorded for the duration of the sag.

A comparison of the results using the proposed method and the simulation is shown in Figure 8.

Observing the results shown in Figure 8, it can be concluded that the results of the mathematical methodology proposed are very close to the simulated results. The maximum deviation of results is measured to be 0.7 %. Thus it can be concluded that the methodology is accurate when applied to this test system. Next, the methodology proposed is applied on a larger system, such as the fourteen bus IEEE system.

3.2. Fourteen Bus IEEE Test System

The methodology proposed is applied on the IEEE fourteen bus interconnected system in an attempt to further validate its accuracy. The aim is to further validate its accuracy of the proposed methodology. The validation procedure and results are shown in the next sections. The fourteen-bus IEEE interconnected system is presented in Figure 9. The system parameter values are available upon request [19].

As shown in Figure 9, the test system consists of fourteen buses, with buses 1and 2 having generators connected to them, and buses 3, 6 and 8 being supplied by synchronous condensers. The voltage sag is caused in the system by the introduction of a new load ‘Zl’ on bus fourteen of the system, consisting of induction motors [6]. The steps of the proposed methodology are applied to the system in order to illustrate its applicability and accuracy.

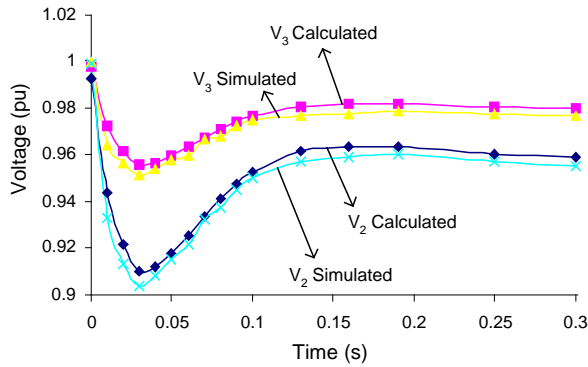


Figure 8. Comparison of results from proposed methodology and PSCAD simulation

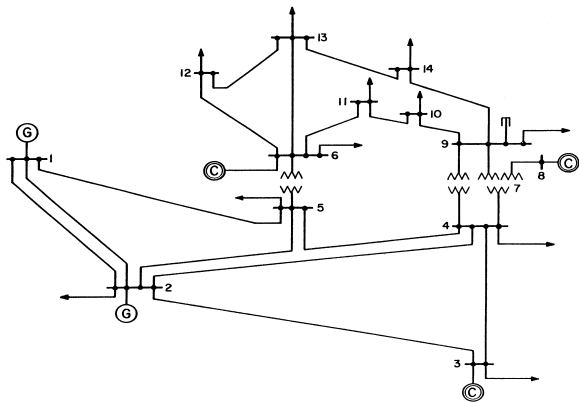


Figure 9. The IEEE fourteen-bus interconnected system

3.2.1. Step 1- Calculation of ZPCC

In order to calculate the ZPCC, the power system impedances are reduced to one equivalent impedance between the load bus causing the sag (in this case bus 14), and the generator equivalent bus, as described previously. The fourteen-bus system shown in Figure 9 for a load supply point on bus 14 can be reduced in a similar way as previously for the four bus test system. The resulting per unit value of ZPCC for this case is calculated to be:

$$ZPCC = 1.150013 + j0.435424741 \quad (9)$$

Having obtained the value of ZPCC, Step 2 of the proposed methodology is performed.

3.2.2. Step 2- Obtaining Power Waveforms

In this section, the active and reactive power demand waveforms are obtained, for the transient period of the induction motor. The contribution of the generators towards that power demand is also calculated. The value of ZPCC, obtained in the previous section, can be incorporated in a two bus equivalent circuit as shown in Figure 2. The motor demand active and reactive power waveforms can be obtained in the same way as for the four bus system, and are shown in Figure 10.

The generators of the system have to increase the power generated to incorporate the increased demand caused by the switching in of the motor. It is necessary for the application of the methodology to identify the contribution of each of the generators towards the demand increase. The numerical values of the active and reactive power load waveforms were used to produce the value of the

generator power waveforms as in the previous case. Having calculated the values of load and generator power, and the value of current according to equations (2)-(5), the generator active and reactive power calculated are shown in Figure 11 and Figure 12, respectively. The transient period is assumed to start at 10 seconds as the system under study needs to initialize first.

3.2.3. Step 3- Instantaneous Load Flow Calculation

Having established the active and reactive power numerical values for the motor and the system generators, the transmission line and load data were used in a Mathcad program. The program implementing Gauss-Seidel technique in Mathcad is an extension of the program shown in Figure 7.

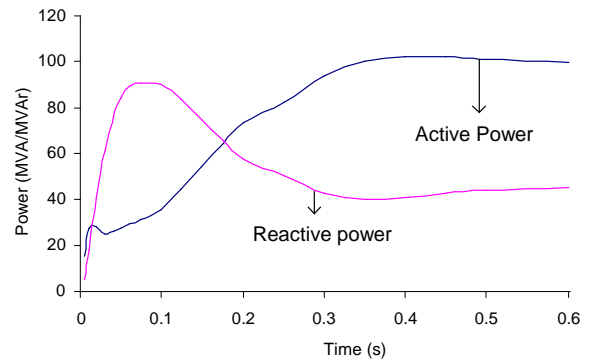


Figure 10. Motor load power demand for load switching on bus 14

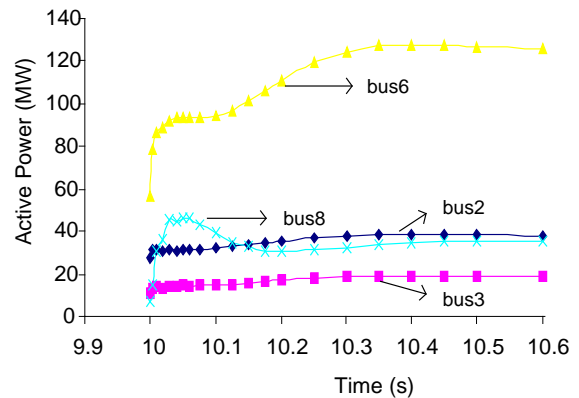


Figure 11. System generators' active power (MW) with load switching on bus 14

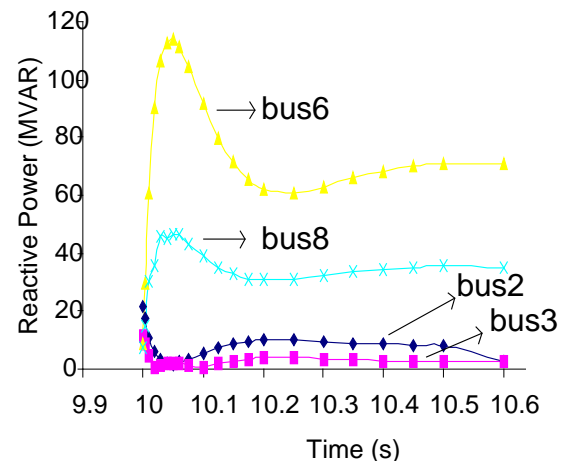


Figure 12. System generators reactive power (MVAR) with load switching on bus 14

Table 3. Methodology Results for IEEE Test System (Buses 2-7)

Time	Bus 2	Bus 3	Bus 4	Bus 5	Bus 6	Bus 7
	0.99999	0.99999	0.999	0.99999	0.999	0.9912
10.005	0.99999	0.99999	0.992	0.99999	0.998	0.971
10.01	0.99999	0.99999	0.988	0.993	0.994	0.956
10.02	0.99999	0.99999	0.98	0.992	0.992	0.934
10.03	0.99999	0.99999	0.972	0.989	0.989	0.921
10.04	0.99999	0.99999	0.978	0.9884	0.99	0.92
10.05	0.99999	0.99999	0.971	0.9872	0.9896	0.918
10.06	0.99999	0.99999	0.973	0.9871	0.9887	0.916
10.075	0.99999	0.99999	0.974	0.989	0.9889	0.92
10.1	0.99999	0.99999	0.978	0.989	0.991	0.93
10.125	0.99999	0.99999	0.978	0.991	0.993	0.936
10.15	0.99999	0.99999	0.981	0.989	0.9956	0.941
10.175	0.99999	0.99999	0.9803	0.9927	0.995	0.9454
10.2	0.99999	0.99999	0.9821	0.9913	0.993	0.94645
10.25	0.99999	0.99999	0.984	0.9931	0.9945	0.9467
10.3	0.99999	0.99999	0.9836	0.9899	0.9934	0.9445
10.35	0.99999	0.99999	0.9818	0.991	0.9943	0.942
10.4	0.99999	0.99999	0.9827	0.997	0.9924	0.9399
10.45	0.99999	0.99999	0.9829	0.988	0.9959	0.93876
10.5	0.99999	0.99999	0.982	0.989	0.9962	0.94
10.6	0.99999	0.99999	0.9798	0.9894	0.9951	0.9401

Table 4. Methodology Results for IEEE Test System (Buses 8-14)

Time	Bus 8	Bus 9	Bus 10	Bus 11	Bus 12	Bus 13	Bus 14
	0.99999	0.979	0.9763	0.984	0.9843	0.9784	0.9587
10.005	0.9982	0.941	0.9432	0.9643	0.9678	0.9454	0.82421
10.01	0.9981	0.916	0.9162	0.951	0.9546	0.91872	0.72578
10.02	0.99812	0.87	0.882	0.931	0.9378	0.8891	0.602
10.03	0.996	0.848	0.862	0.9183	0.92532	0.8711	0.541
10.04	0.997	0.841	0.8575	0.9178	0.924	0.86784	0.51864
10.05	0.9952	0.8385	0.8537	0.91476	0.921	0.86589	0.51623
10.06	0.995	0.841	0.8564	0.91532	0.9226	0.8662	0.52765
10.075	0.997	0.848	0.862	0.9187	0.92689	0.8747	0.5505
10.1	0.998	0.8646	0.8756	0.9264	0.9325	0.8864	0.59914
10.125	0.9982	0.8789	0.8886	0.931	0.938	0.8963	0.6381
10.15	0.998	0.88946	0.8957	0.9383	0.9435	0.89998	0.6646
10.175	0.9982	0.8956	0.902	0.941	0.9461	0.90894	0.681
10.2	0.9981	0.89978	0.90589	0.941	0.9479	0.90621	0.68996
10.25	0.9983	0.89982	0.907	0.942	0.94832	0.9013	0.68963
10.3	0.99876	0.8976	0.905	0.9399	0.94457	0.89834	0.67892
10.35	0.998	0.892	0.8996	0.9382	0.94459	0.89789	0.66641
10.4	0.997	0.889	0.8952	0.93746	0.9416	0.8938	0.6571
10.45	0.9969	0.8874	0.8937	0.9376	0.94001	0.8921	0.64812
10.5	0.996	0.8889	0.89312	0.9368	0.9421	0.8941	0.6472
10.6	0.997	0.8893	0.892	0.938	0.9412	0.89276	0.6481

The obtained results from the program for the case studied in this section are presented in Table 3, for buses 2-7 and Table 4, for buses 8-14. In the above tables, the time in seconds and the voltage on the load buses in per unit values are shown during the voltage sag.

3.2.4. Step 4- Instantaneous Load Flow Calculation

In order to verify the proposed methodology and the result obtained through the load flow technique shown in Table 3 and Table 4, the fourteen-bus system was simulated in PSCAD, with the motor load switched in the system at bus fourteen. The bus voltage values were recorder for the time steps used in the proposed method. A comparison of the calculated results using the proposed method and the obtained simulation results is shown in Figure 13. The comparison results are shown in terms of maximum accuracy deviation for each bus. The data is presented in this form due to the large amount of data produced. Detailed data are available upon request. The results in

Figure 13 show that the maximum error between calculation and simulation is less than 1.2 %. Therefore it is once again verified that the methodology proposed is within acceptable limits and can be used in predicting voltage sag occurring on the system buses, caused by heavy load switching. Thus it can be concluded that the methodology is accurate when applied to this test system.

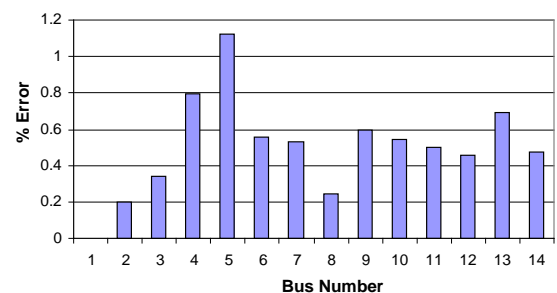


Figure 13. Maximum percentage error between proposed methodology and PSCAD simulation for connecting the load on bus 14

4. Conclusions

The various steps of the proposed methodology have been discussed, explained, and validated using a four bus system and the IEEE fourteen bus interconnected test systems. For validation purposes, the proposed methodology was compared and validated through PSCAD simulation and MathCAD mathematical programming. It is shown that the calculated output of the methodology accurately represents the voltage sag sensed on the various buses. Further work would be accommodate the solution of all the mathematical expressions of the methodology proposed into one common environment to provide a complete package for voltage sag prediction caused by large induction motors for interconnected systems.

Appendix A: Basic Machine Theory

The generalized machine model transforms the stator windings into equivalent commutator windings, using the dq0 transformation as follows:

$$\begin{bmatrix} U_d \\ U_q \\ U_0 \end{bmatrix} = \begin{bmatrix} \cos(\theta) & \cos(\theta - 120^\circ) & \cos(\theta - 240^\circ) \\ \sin(\theta) & \sin(\theta - 120^\circ) & \sin(\theta - 240^\circ) \\ 1/2 & 1/2 & 1/2 \end{bmatrix} \cdot \begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix}$$

The three-phase rotor winding may also be transformed into a two phase equivalent winding, with additional windings added to each axis to fully represent that particular machine, as is shown in Figure 4 [20,21].

Where,

k = Amortisseur windings

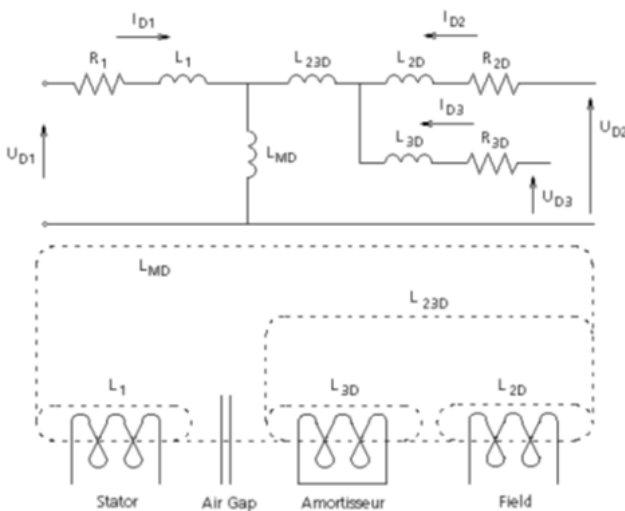
f = Field windings

abc = Stator windings

d = Direct-Axis (d-axis) windings

q = Quadrature-Axis (q-axis) windings.

The d-axis equivalent circuit for the generalized machine is shown in figures below. The second figure illustrates the flux paths associated with various d-axis inductances:



Referring to the above figures, the following equations can be derived:

$$\begin{bmatrix} U_{D1} - v \cdot \Psi_q - R_1 \cdot i_{D1} \\ U_{D2} - R_{2D} \cdot i_{D2} \\ U_{D3} - R_{3D} \cdot i_{D3} \end{bmatrix} = L_D \cdot \frac{d}{dt} \begin{bmatrix} i_{D1} \\ i_{D2} \\ i_{D3} \end{bmatrix}$$

Where

$$L_D = \begin{bmatrix} L_{MD} + L_1 & L_{MD} & L_{MD} \\ \sin(\theta) & \sin(\theta - 120^\circ) & L_{MD} + L_{23D} \\ L_{MD} & L_{MD} + L_{23D} & L_{MD} + L_{23D} + L_{3D} \end{bmatrix}$$

$$\Psi_q = L_1 \cdot i_{q1} + L_{MD} \cdot (i_{Q1} + i_{Q2} + i_{Q3})$$

$$v = \frac{d\theta}{dt}$$

Similar equations hold for the q-axis except the speed voltage term v. Ψ_d , is positive, and:

$$\Psi_d = L_1 \cdot i_{D1} + L_{MD} \cdot (i_{D1} + i_{D2} + i_{D3})$$

Inversion of the first Equation gives the standard state variable form $X0 = AX + BU$ with state vector X consisting of the currents, and the input vector U, applied voltages. That is:

$$\frac{d}{dt} \begin{bmatrix} i_{D1} \\ i_{D2} \\ i_{D3} \end{bmatrix} = L_D^{-1} \cdot \begin{bmatrix} -v \cdot \Psi_q - R_1 \cdot i_{D1} \\ -R_{2D} \cdot i_{D2} \\ -R_{3D} \cdot i_{D3} \end{bmatrix} + L_D^{-1} \cdot \begin{bmatrix} U_{D1} \\ U_{D2} \\ U_{D3} \end{bmatrix}$$

$$\frac{d}{dt} \begin{bmatrix} i_{Q1} \\ i_{Q2} \\ i_{Q3} \end{bmatrix} = L_Q^{-1} \cdot \begin{bmatrix} -v \cdot \Psi_d - R_1 \cdot i_{Q1} \\ -R_{2D} \cdot i_{Q2} \\ -R_{3D} \cdot i_{Q3} \end{bmatrix} + L_Q^{-1} \cdot \begin{bmatrix} U_{Q1} \\ U_{Q2} \\ U_{Q3} \end{bmatrix}$$

The above equations are easy to integrate and hence are solved using trapezoidal integration to obtain the currents. The torque equation is given as:

$$T = \Psi_q \cdot i_{D1} - \Psi_d \cdot i_{Q1}$$

and the mechanical dynamic equation for motor operation is:

$$\frac{dv}{dt} = \frac{T - T_{MECH} - D \cdot v}{J}$$

References

- [1] IEEE, "Recommended practice for monitoring electric power quality", *IEEE*, pp. 1-80, 1995.
- [2] Polycarpou, A, Nouri, H, Davies, T and Ciric, R "An overview of voltage sag theory, Effects and Equipment compatibility", UPEC, Bristol, UK, 2004.
- [3] JM Milanovic, J.V, Vegunta,S.C, Aung, M. T "The Influence of Induction Motors on Voltage Sag Propagation-Part I: Accounting for the Change in Sag Characteristics", *IEEE Transactions on Power Delivery*, vol. 23, No. 2, pp. 1063-1071, 2008.
- [4] JM Milanovic, J.V, Vegunta,S.C, Aung, M. T "The Influence of Induction Motors on Voltage Sag Propagation-Part II: Accounting for the Change in Sag Performance at LV Buses", *IEEE Transactions on Power Delivery*, vol. 23, No. 2, pp. 1072-1078, 2008.

- [5] Huweg, A. F, Bashi, S. M.M and Mariun, S.N “Application of inverter based shunt device for voltage sag mitigation due to starting of an induction motor load”, *CIREC 2005, 18th International Conference and Exhibition on Electricity Distribution*, pp. 1-5, 2005.
- [6] Hsu, C.T, Chuang, H.J and Chen, C.S “Power Quality Assessment of Large Motor Starting and Loading for the Integrated Steel-Making Cogeneration Facility”, *Industry, IEEE Transactions on Applications*, vol. 43, No. 2, pp. 395-402, 2007.
- [7] Polycarpou, A and Nouri, H “Analysis and simulation of bus loading conditions on voltage sag in an interconnected network”, *UPEC, Staffordshire University, UK*, 2002.
- [8] Bollen, M.H.J “The Influence of Motor Reacceleration on Voltage Sags”, *IEEE Transactions on Industry Applications*, vol. 31, pp. 667-674, 1995.
- [9] Gomez, J.C, Morcos, M.M, Reineri, C and Campetelli, G, “Induction Motor Behavior Under Short Interruptions and Voltage Sags”, *IEEE Power Engineering Review*, pp. 11-15, 2001.
- [10] Nouri, H and Polycarpou, A “The influence of double cage motors on voltage sag and power quality”, *Medpower, Athens, Greece*, 2002.
- [11] Becker, C et al., “Proposed Chapter 9 for Predicting Voltage Sags (Dips) in Revision to IEEE Std 493, the Gold Book”, *IEEE Transactions on Industry Applications*, vol. 30, pp. 805-821, 1994.
- [12] Bollen, M.H.J. “Voltage Sags in Three-Phase Systems”, *IEEE Power Engineering Review*, pp. 8-15, September 2001.
- [13] Yalcinkaya, G, Bollen, M.H.J and Crossley, P.A. “Characterization of Voltage Sags in Industrial Distribution Systems”, *IEEE Transactions on Industry Applications*, vol. 34, pp. 682-688, 1998.
- [14] Bollen, M.H.J. “Voltage sag indices-Draft 2”, working document for IEEE P1564, November 2001.
- [15] Nouri, H and Polycarpou, A “Mathematical development, Investigation and Simulation of a new Quadratic Voltage Index”, *UPEC, Newcastle*, September 2006.
- [16] Polycarpou, A and Nouri, H “Investigation into the compatibility and effectiveness of a new mathematical online voltage sag index”, *Int. Journal Power and Energy Conversion*, vol. 2, No. 1, pp. 46-58, 2010.
- [17] Polycarpou, A and Nouri, H “A New Index for On Line Critical Voltage Calculation of Heavily Loaded Feeders”, *PowerTech, St. Petersburg, Russia*, 2005.
- [18] Polycarpou, A and Nouri, H “Validation of a Proposed Voltage Sag Prediction Methodology for Interconnected systems during Motor Starting”, *IEEE 44th Int. Universities Power Engineering Conference*, Glasgow, Scotland, UK, September 2009.
- [19] Polycarpou, A “Identification of Voltage Sag Indices in Electrical Power Systems”, PhD Thesis, UWE, UK, April 2006.
- [20] PSCAD USER’S GUIDE, “A Comprehensive Resource for EMTDC” 211 Commerce Drive, Chapter 7: Rotating Machines, pp 126-132.
- [21] I. M. Canay, “Causes of Discrepancies on Calculation of Rotor Quantities and Exact Equivalent Diagrams of the Synchronous Machine,” *IEEE Transactions*, Vol. PAS-88, No.F, p. 1114-1120, July 1969.